


Proteus VSM for PIC10/12

System Level Simulation for Microchip Technologies™ PIC10/12 Variants.

Summary

Proteus Virtual System Modelling (VSM) combines mixed mode SPICE circuit simulation, animated components and microprocessor models to facilitate co-simulation of complete microcontroller based designs. The 'Proteus VSM for PIC10/12' product includes the following main software modules:

- Professional Schematic Capture module
- ProSPICE professional Simulation Engine
- All supported VSM microcontroller variants in the PIC10/12 Family.
- All of the Proteus Embedded Simulation Peripheral Libraries.
- VSM Studio IDE with automatic compiler configuration.
- Over 10,000 standard simulation models.

 *Proteus VSM for PIC® Bundle products are ideal if you need to simulate more than one family of PIC micro-controllers.*

Variants

The following is a current list of supported variants in the PIC10/12 family:

- PIC10F200, PIC10F202, PIC10F204, PIC10F206
- PIC10F220, PIC10F222, PIC10F320, PIC10F322
- PIC10LF320, PIC10LF322, PIC12C508A, PIC12C509A
- PIC12C671, PIC12C672, PIC12CE518, PIC12CE519
- PIC12F508, PIC12F509
- PIC12CE673, PIC12CE674, PIC12F510, PIC12F609
- PIC12F615, PIC12F629, PIC12F675, PIC12F683
- PIC12F1501, PIC12LF1501, PIC12F752, PIC12HV752
- PIC12F1822, PIC12LF1822, PIC12F1840, PIC12LF1840
- PIC12F1571, PIC12F1572, PIC12LF1571, PIC12LF1572

Features

We believe our simulation models are the most accurate and the most complete on the market today. A summary of model capabilities is listed below:

- Fully simulates the entire instruction set.
- Supports all port and other I/O pin operations including weak pullup.
- Supports all timers including watchdog timer, sleep mode and wake-up from sleep.
- Supports Analogue-to-Digital Conversion (ADC) module including support for voltage reference pins.
- Supports the Analogue Comparator and Voltage Reference modules.
- Supports internal code and data EEPROM memory inc. code protection and data persistence.
- Supports all interrupt modes including pin change interrupts.
- Supports the Complementary Output Generator (CWG) module in appropriate variants.
- Supports the Configurable Logic Cell (CLC) module in appropriate variants.
- Supports the Pulse Width Modulation (PWM) module in appropriate variants.
- Supports the Numerical Controlled Oscillator (NCO) module in appropriate variants.
- Supports the Complementary Waveform Generator (CWG) module in appropriate variants.
- Internally generated processor clock for performance. Event timing accurate to one clock period.
- Provides internal consistency checks on code (e.g. execution of invalid op-codes, illegal memory accesses, stack overflow checking, etc.).
- more... Fully integrated in to the VSM source level debugging system.
- more... Fully integrated into the Proteus Diagnostic Control System.

Limitations

The following is a listing of known limitations in the current version of the PIC10/12:

- x No significant functional limitations.
- x HFINTOSC as CLC1 input is not supported for efficiency reasons.
- x IVR Internal Voltage Regulator and VREGCON register effect are not modeled.
- x BORCON register effect are not modeled.

Compilers

Supported Third Party Compilers

Proteus VSM models will fundamentally work with the exact same HEX file as you would program the physical device with. However, far more debugging information is available when using a compiler to write the firmware and providing these object files to Proteus in place of the HEX file provides a much richer working environment.

We recommend you use the free Labcenter VSM Studio IDE. This will greatly simplify the task as it will automatically configure supported compilers to work with a Proteus VSM simulation.

If you prefer to work inside your own IDE then you will need to set your compiler options manually. After compiling for debug, all you need to do is specify the debug file from the compiler as the program property of the microcontroller on the schematic.

VSM Studio supported toolchains

- IAR
- HI-TEC
- Microchip XC8
- Microchip XC16
- Proton+
- CCS
- Source Boost
- Byte Craft



With continual development on the Proteus Design Suite we endeavour to keep all content updated with the latest product details. On rare occasions this may not happen immediately, and website content will then be incomplete or inaccurate. We will attempt to correct any such errors as soon as possible, E&OE.