




Proteus VSM Starter Kit for AVR®

Entry Level Product for Atmel Simulation.

Summary

Proteus Virtual System Modelling (VSM) combines mixed mode SPICE circuit simulation, animated components and microprocessor models to facilitate co-simulation of complete microcontroller based designs. The 'Proteus VSM Starter Kit for AVR®' product includes the following main software modules:

- Professional Schematic Capture module
- ProSPICE professional Simulation Engine
- Support for the Proteus VSM Starter Kit for three variants only.
- All of the Proteus Embedded Simulation Peripheral Libraries.
- VSM Studio IDE with automatic compiler configuration.
- Over 10,000 standard simulation models.

 *The full Proteus VSM for AVR Product also includes simulation of the entire range of supported Atmel AVR variants.*

Variants

The following is a current list of supported variants in the AVR® Starter Kit family:

- ATTiny2312, ATmega8, ATmega8535
- Please refer to the full family or bundle products for more complete coverage of AVR microcontrollers.

Features

We believe our simulation models are the most accurate and the most complete on the market today. A summary of model capabilities is listed below:

- Supports the entire instruction set.
- Supports all port and other I/O pin operations.
- Supports all counter/timers including separate prescalers, capture compare and PWM modes.
- Supports watchdog timer.
- Supports serial U(S)ART.
- Supports master slave SPI, USI and TWI serial interfaces.
- Supports Analogue-to-Digital Conversion (ADC) and analogue comparator modules in all modes.
- Supports all internal and external interrupt modes.
- Supports internal code and data EEPROM memory inc. code protection and data persistence.
- Internally generated processor clock for performance. Event timing accurate to one clock period.
- Provides internal consistency checks on code (e.g. execution of invalid op-codes, illegal memory accesses, stack overflow checking, etc.).
- Fully integrated in to the VSM source level debugging system.
- Fully integrated into the Proteus Diagnostic Control System.

Limitations

The following is a listing of known limitations in the current version of the AVR®:

- ✗ Brown-out Reset is not implemented.
- ✗ Power supply voltage changing is not supported.
- ✗ JTAG and other in-circuit debugging interfaces are not supported.
- ✗ External programming of memories is not supported.
- ✗ Electrical characteristics dependency of the temperature is not implemented.

Compilers

Supported Third Party Compilers

Proteus VSM models will fundamentally work with the exact same HEX file as you would program the physical device with. However, far more debugging information is available when using a compiler to write the firmware and providing these object files to Proteus in place of the HEX file provides a much richer working environment.

We recommend you use the free Labcenter VSM Studio IDE. This will greatly simplify the task as it will automatically configure supported compilers to work with a Proteus VSM simulation.

If you prefer to work inside your own IDE then you will need to set your compiler options manually. After compiling for debug, all you need to do is specify the debug file from the compiler as the program property of the microcontroller on the schematic.

VSM Studio supported toolchains

- IAR
- GCC
- Arduino
- ImageCraft
- HP Info Tech
- Bascom AVR



With continual development on the Proteus Design Suite we endeavour to keep all content updated with the latest product details. On rare occasions this may not happen immediately, and website content will then be incomplete or inaccurate. We will attempt to correct any such errors as soon as possible, E&OE.